

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

TED LETAVIC ET AL

Serial No.: 10/015,640

Filed: DECEMBER 10, 2001

Title: HIGH FREQUENCY HIGH VOLTAGE SILICON-ON-INSULATORS
DEVICE WITH MASK VARIABLE INVERSION CHANNELLAND METHOD EX DEVICE WITH MASK VARIABLE INVERSION CHANNELLAND METHOD FOR

FORMING THE SAME

Commissioner for Patents Washington, D.C. 20231

LETTER TO OFFICIAL DRAFTSMAN

Sir:

Enclosed are (2) two sheets of formal drawings for filing in the above-identified application.

Respectfully submitted,

Reg. 26,531

Attorney

(914) 333-9630

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited this date with the United States Postal Service as first-class mail in an envelope addressed to: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231